

003271" 62642460

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FIG.1

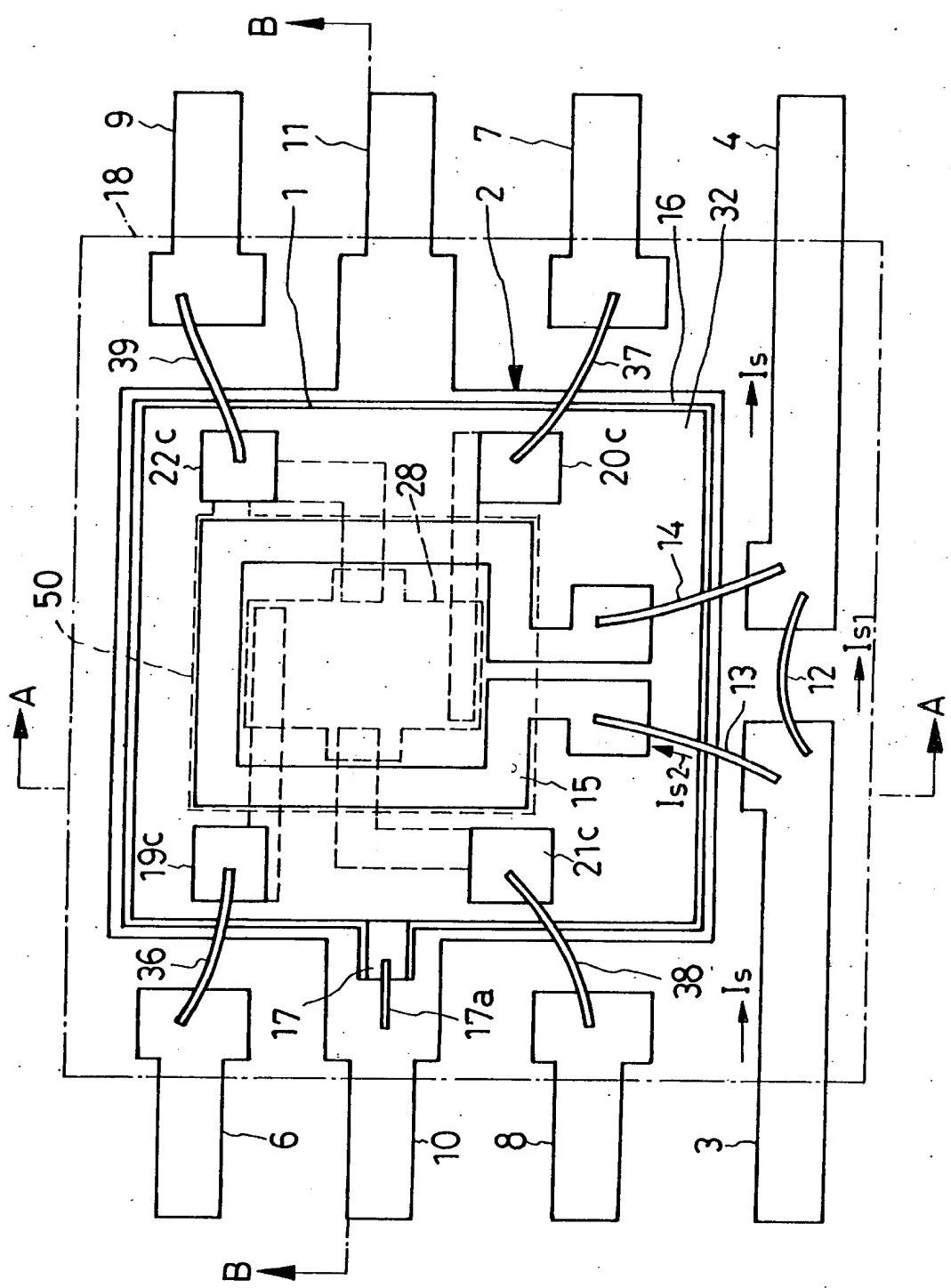


Figure 1 is a plan view of a semiconductor device. The device consists of a central square region (24) with a p-type layer (p) and n+ regions. A current I_c flows downwards through the n+ regions. The central region is surrounded by a ring-like structure (24) and is connected to a larger rectangular region (15) via a narrow neck. The entire device is on a substrate (31). Various regions are labeled with numbers: 19c, 21c, 22c, 23, 25, 26, 27, 28, 29, 30, and 31. A coordinate system (X, Y) is shown at the bottom, and a magnetic field vector B is indicated on the right side.

FIG.3

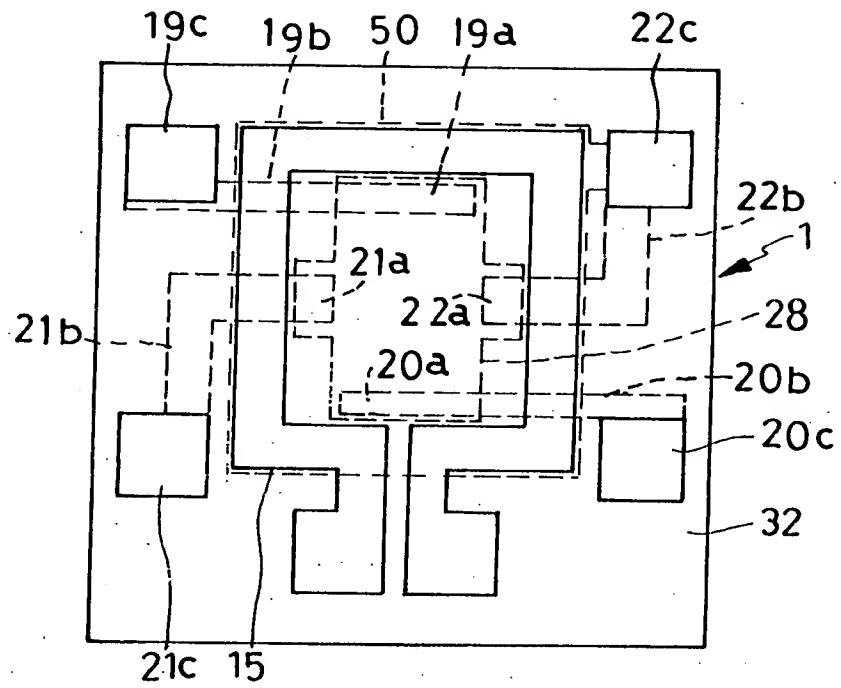


FIG.4

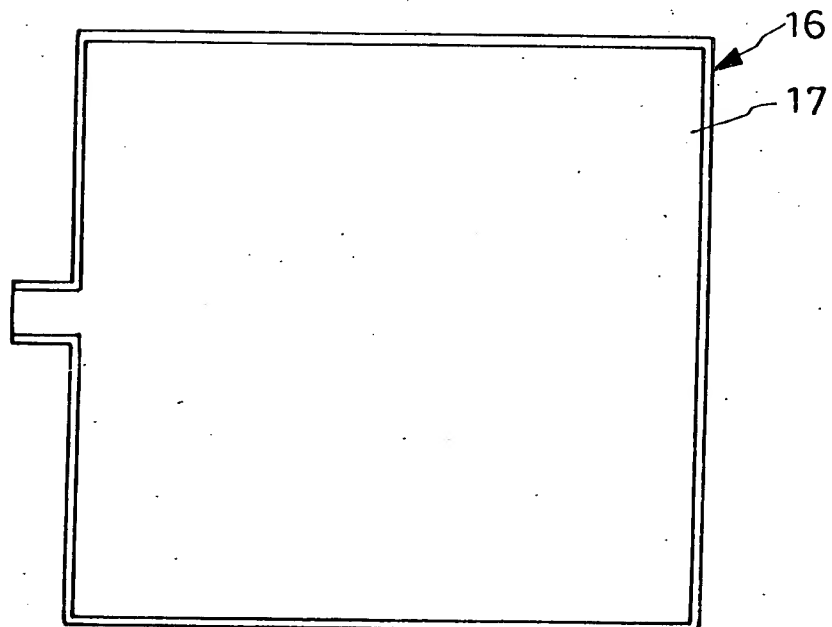


FIG.5

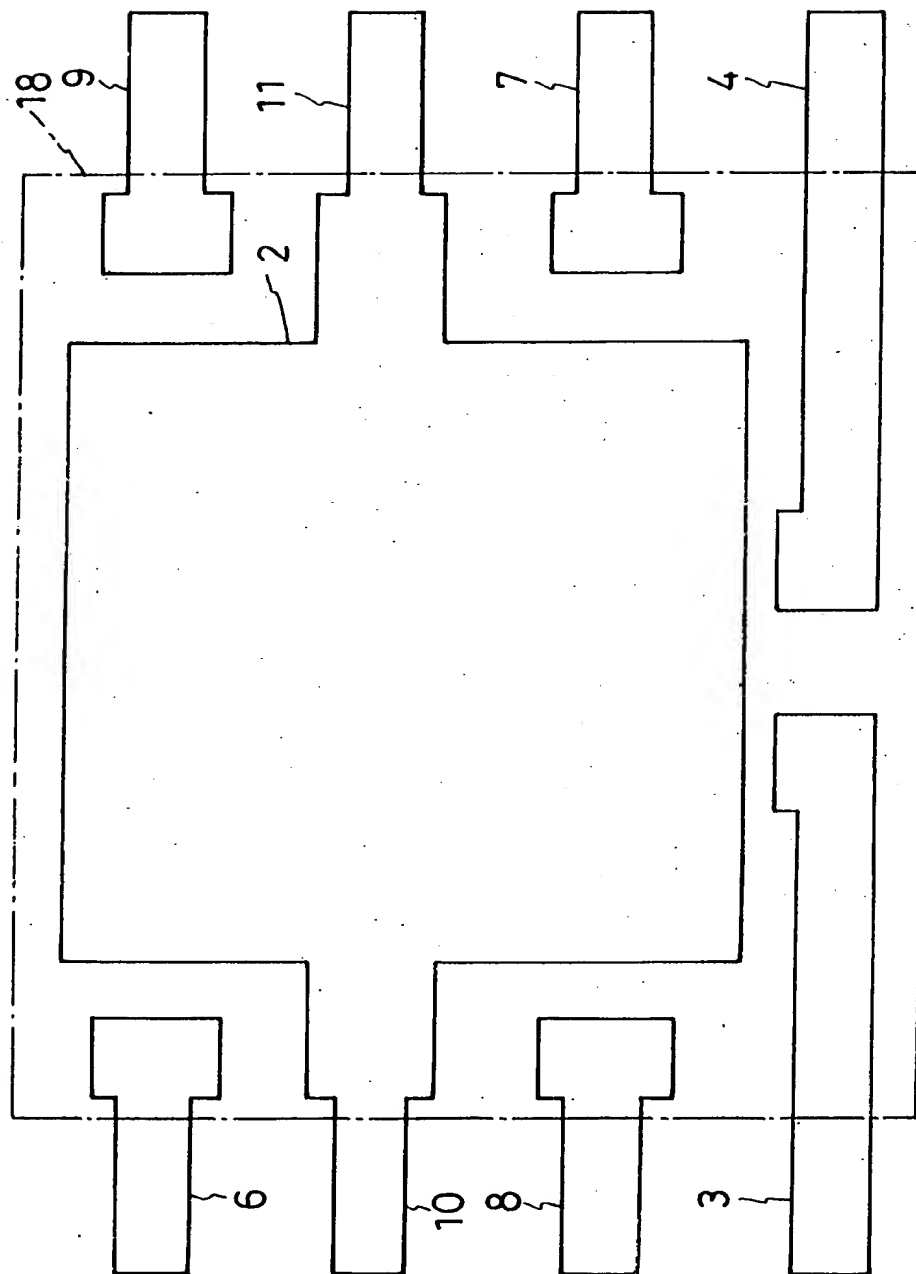


FIG.6

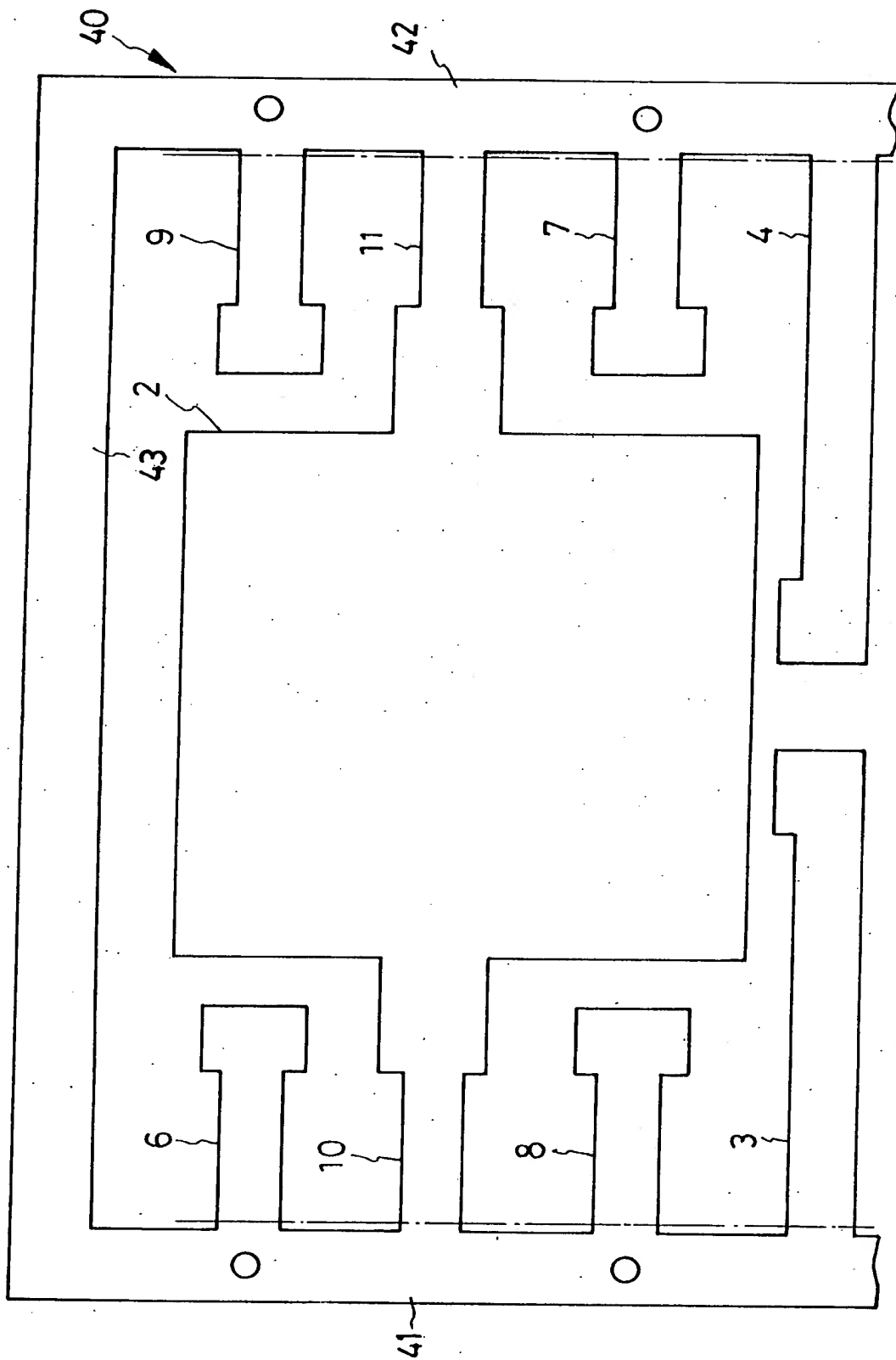


FIG. 8

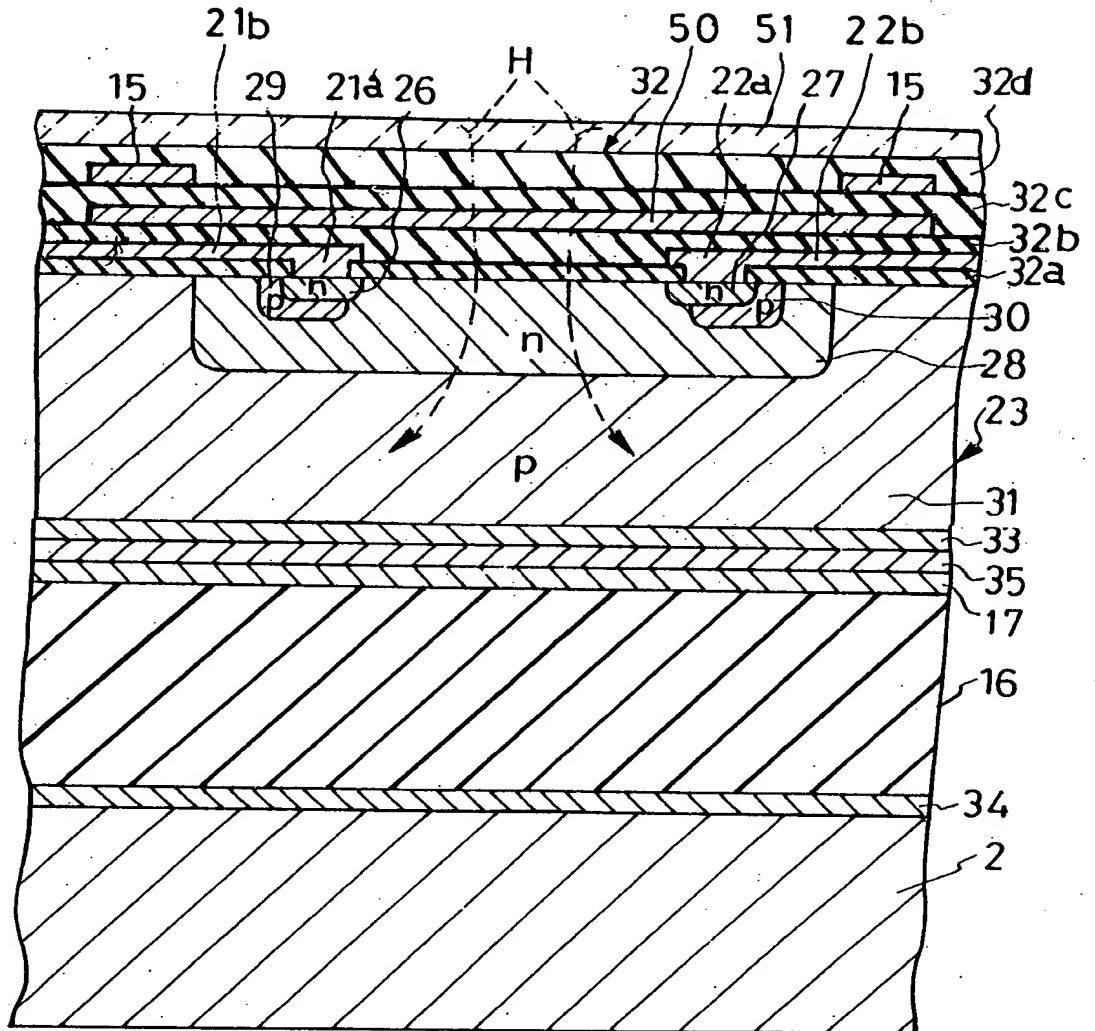


FIG.9

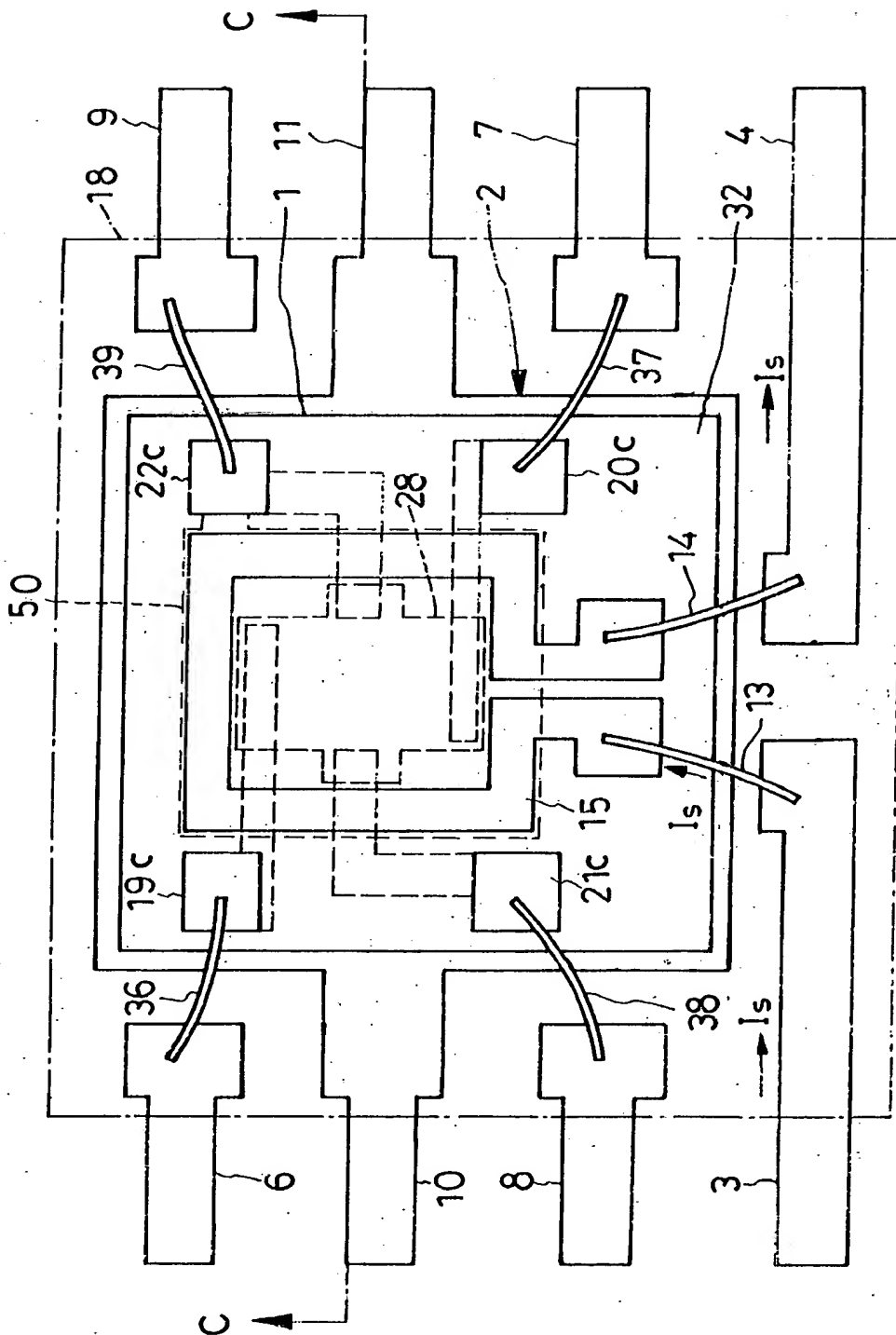


FIG. 10

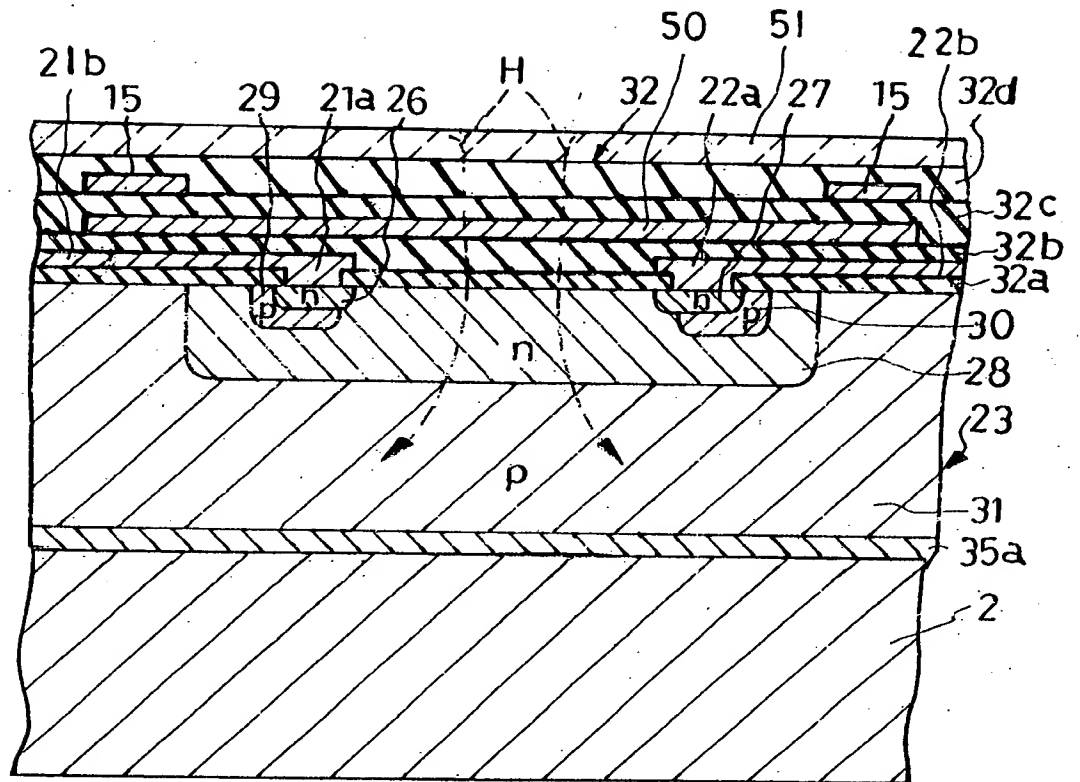


FIG. 11

